



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-34026

Manjeri Krishnan, et al.

Art Unit: 2818

Serial No.: 10/630,963

Examiner:

Filed: July 30, 2003

Conf. No: 1767

For: Electrical Fuse Control of Memory Slowdown

**LETTER TO THE OFFICIAL DRAFTSPERSON**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)  
I hereby certify that the above correspondence is being deposited  
with the U.S. Postal Service as First Class Mail in an envelope  
addressed to: Commissioner for Patents, P.O. Box 1450,  
Alexandria, VA, on April 20, 2004.

  
Robin E. Barnum

Please find enclosed the formal drawings for the subject case. Charge any necessary fees to  
Deposit Account No. 20-0668. **This form is submitted in triplicate.**

Respectfully submitted,



Robert D. Marshall, Jr.  
Attorney for Applicant(s)  
Reg. No. 28,527

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